

Serial No. 09/996,714
Amdt. dated March 24, 2004
Reply to Office Action of November 5, 2003

Docket No. K-0343

Amendments to the Claims:

This listing of claims will replace all prior versions, and listings, of claims in the application:

Listing of Claims:

1. (Currently Amended) A Plasma Display Panel (PDP) PDP comprising:
~~a pair of sustain electrodes formed at a peripheral portion of on~~ an upper substrate near a peripheral portion of a discharge cell; and
~~a trigger electrode formed on the upper substrate at the center of the upper substrate between the pair of electrodes.~~

2. (Currently Amended) A method for driving a PDP Plasma Display Panel (PDP) including a reset period, an address period, and a sustain period, comprising ~~the steps of~~:
~~supplying, during the reset period, a reset pulse to a trigger electrode formed at the center [[of]] between a pair of electrodes formed on an upper substrate near a peripheral portion of a discharge cell during the reset period;~~
~~supplying a scan pulse to the trigger electrode during the address period;~~
~~supplying a data pulse synchronized with the scan pulse to an address electrode formed on a lower substrate opposing the upper substrate during the address period;~~

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alternately applying a sustain pulse to [[a]] the pair of ~~sustain~~ electrodes ~~formed at~~
~~a peripheral portion of the upper substrate~~ during the sustain period; and
applying a trigger pulse to the trigger electrode during the sustain period.

3. (Currently Amended) The method of claim 2, further comprising ~~the step of~~
supplying a direct current voltage to the address electrode during the reset period.

4. (Original) The method of claim 2, wherein the trigger pulse has a frequency
higher n times than that of the sustain pulse.

5. (Original) The method of claim 2, wherein the trigger pulse has a frequency
higher two times than that of the sustain pulse.

6. (Currently Amended) The method of claim 2, wherein the trigger pulse is
synchronized with the sustain pulse applied to the pair of the ~~sustain~~ electrodes and then is
supplied to the trigger electrode.

7. (Original) The method of claim 2, wherein the trigger pulse has a lower voltage
level than the sustain pulse.

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8. (Currently Amended) The method of claim 2, further comprising the step of supplying the scan pulse to one of the pair of the ~~sustain~~ electrodes during the address period.

9. (New) A Plasma Display Panel (PDP) comprising:
a pair of electrodes formed on a first substrate; and
a single trigger electrode formed on the upper substrate between the pair of electrodes.

10. (New) The PDP of claim 9,
wherein the pair of electrodes are formed on the first substrate near a peripheral portion of a discharge cell, and
wherein the single trigger electrode is formed at the center between the pair of electrodes.

11. (New) The PDP of claim 9, further comprising:
an address electrode formed on a second substrate opposing the first substrate;
and
barrier ribs formed between the first and second substrates.

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12. (New) The PDP of claim 11, further comprising:
- a first dielectric layer formed on the pair of electrodes, the single trigger electrode and the first substrate;
- a passivation film formed on the first dielectric layer;
- a second dielectric layer formed on the first substrate, the address electrode and the barrier ribs; and
- a phosphor layer deposited on surfaces of the second dielectric layer and the barrier ribs.
13. (New) The PDP of claim 11, wherein the address electrode is at a position orthogonal to the first and second sustain electrodes.
14. (New) The PDP of claim 1, wherein one of the pair of electrodes is a scan/sustain electrode and the other is a common electrode.
15. (New) The PDP of claim 1, wherein the trigger electrode is formed at an equal distance from each of the pair of electrodes.

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16. (New) The method of claim 2, wherein one of the pair of electrodes is a scan/sustain electrode and the other is a common electrode.

17. (New) The method of claim 2, wherein the trigger electrode is formed at an equal distance from each of the pair of electrodes.

18. (New) The PDP of claim 9, wherein one of the pair of electrodes is a scan/sustain electrode and the other is a common electrode.

19. (New) The PDP of claim 9, wherein the trigger electrode is formed at an equal distance from each of the pair of electrodes.